



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Kenji TAKASE

Art Unit:

2818

Application No.:

10/791,750

Examiner:

Tu Tu V. Ho

Filed:

March 4, 2004

Conf.:

5270

For:

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD FOR

SAME

LETTER REQUESTING INITIALED PTO 1449 FORM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DEC 13 2006

Sir:

In reviewing the above-captioned application file upon allowance, the undersigned has noted that acknowledgement was not received for the PTO 1449 Form filed with the Information Disclosure Statement on March 4, 2004.

Accordingly, a copy of the unacknowledged PTO 1449 Form is attached hereto. The Examiner is respectfully requested to return the initialed form to the undersigned as soon as possible.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §1.16 or under 37 C.F.R. §1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

<u>B</u>

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Form PTO-1449

INFORMATION DISCLOSURE CITATION

ATTY. DOCKET NO. 0951-0133P	APPLICATION NO. NEW

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APPLICANT Kenji TAKASE	
FILING DATE March 4, 2004	GROUP

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